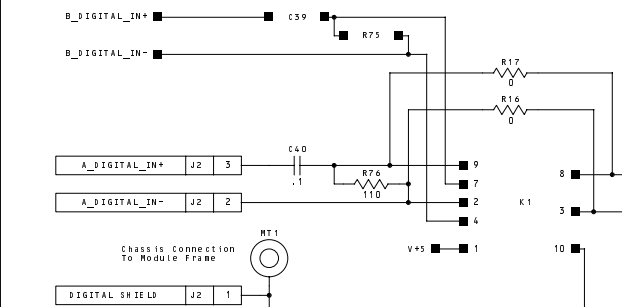


REVISIONS				
REV	ECO #	DESCRIPTION	DATE	APPROVAL
A		INITIAL RELEASE	27JUNE01	L. STANTON
B	4668	Change R10,15,30,31,75,76	27SEPT01	L. STANTON
C	4727	ADD RESISTOR NETWORK B R33	21DEC01	B. NGUYEN

DIGITAL INPUT

FROM pg. 2, B1



Chassis Connection To Mezzanine Board

ANALOG INPUTS

A INPUT L +	J1	3
A INPUT L -	J1	2
A INPUT L SHD	J1	1

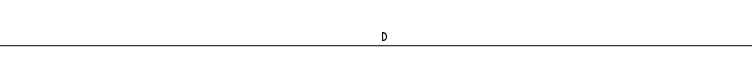
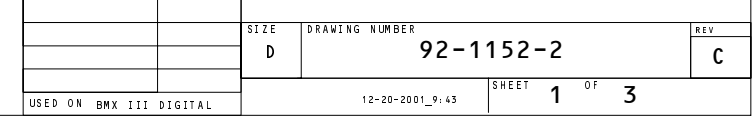
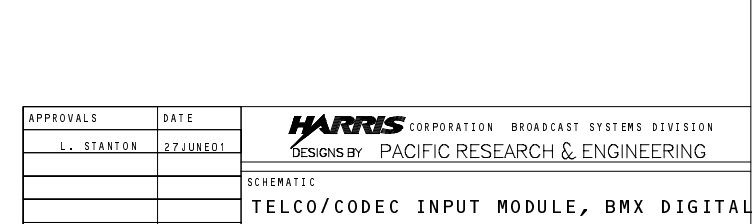
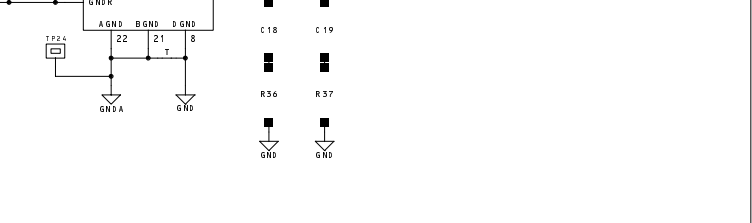
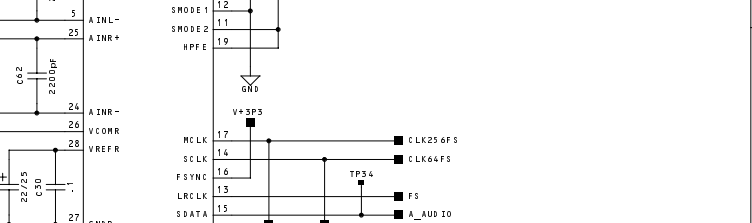
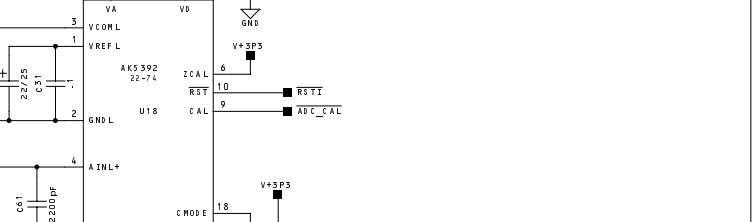
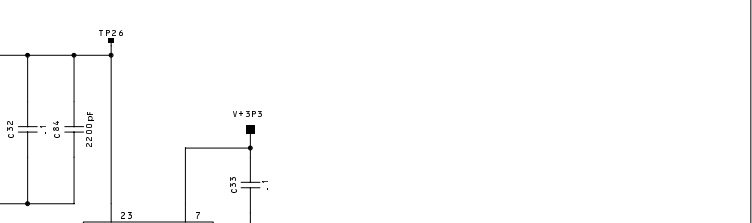
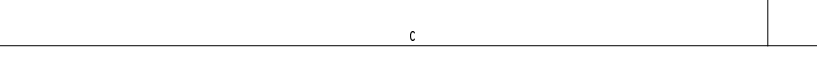
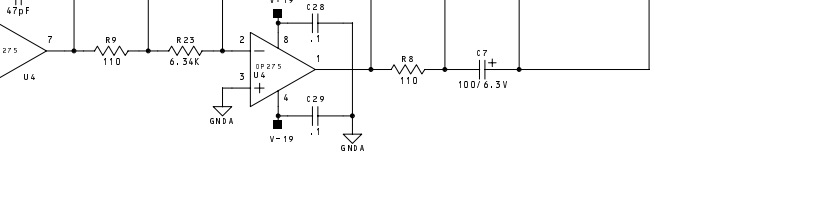
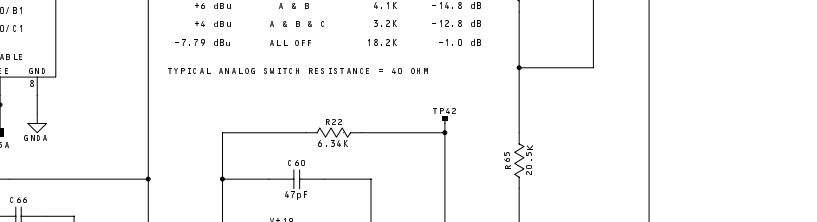
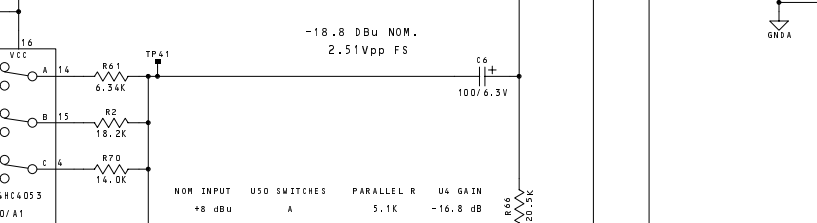
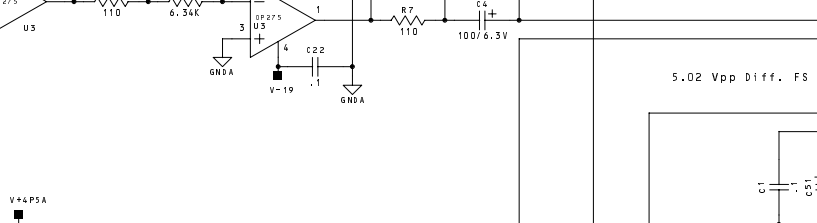
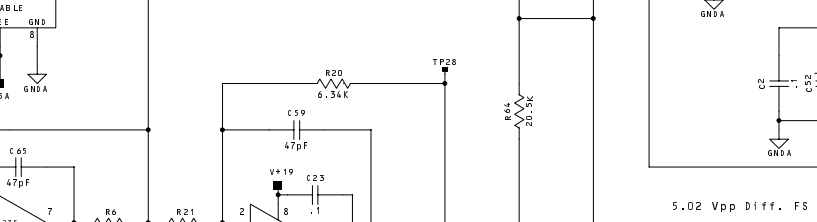
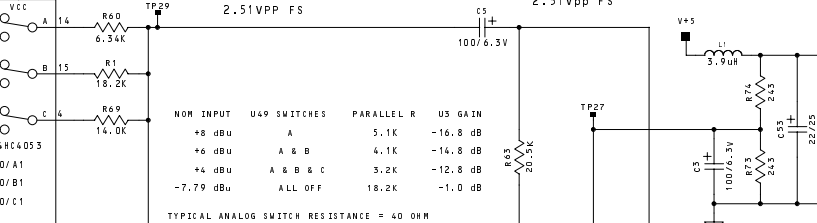
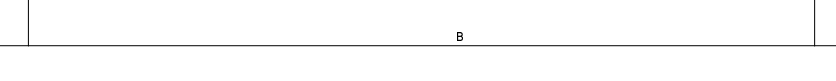
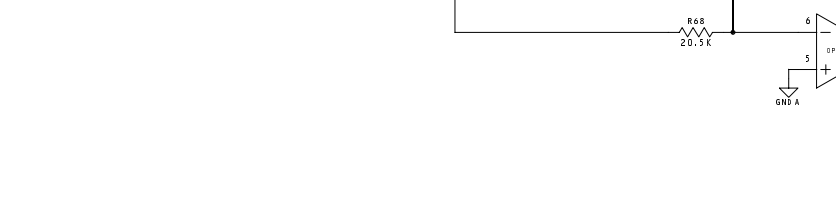
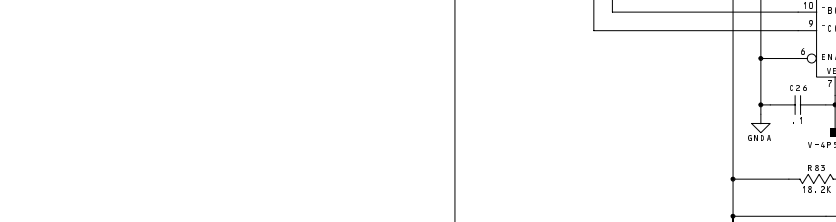
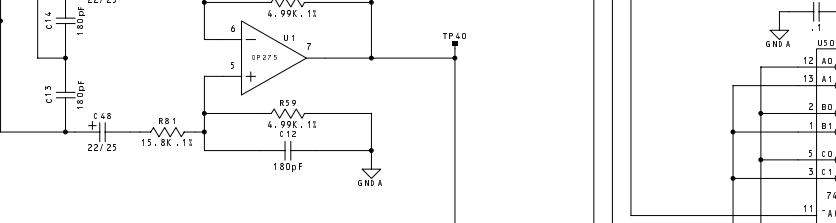
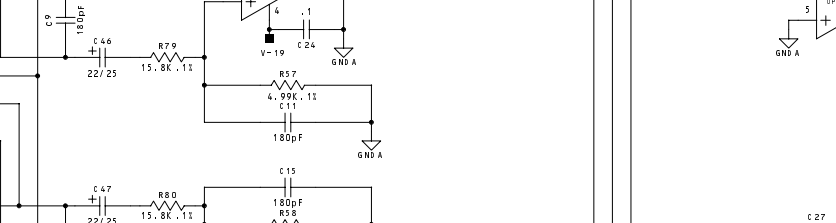
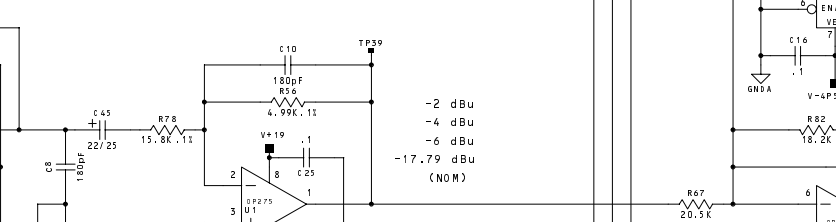
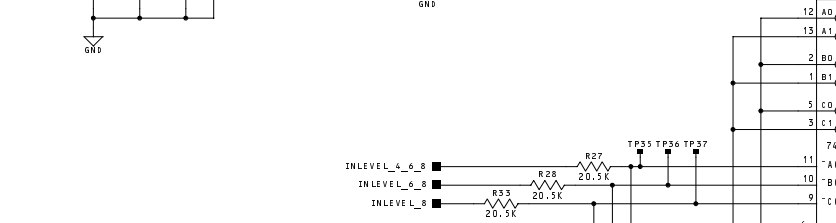
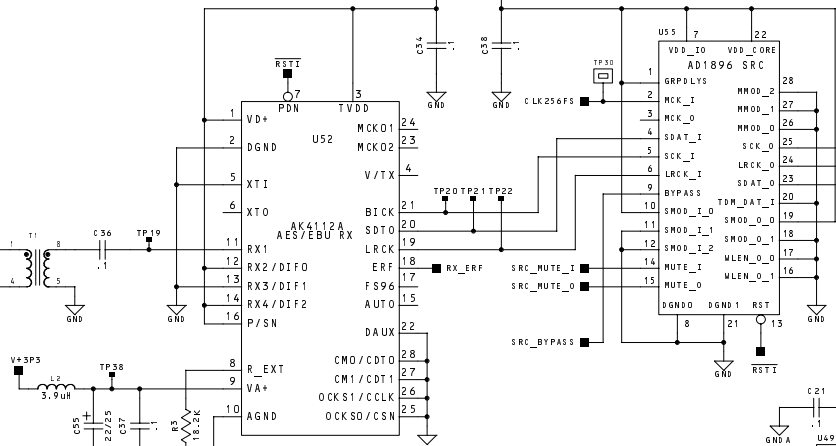
B ANALOG L IN+
FROM pg. 2, B1
B ANALOG L IN-
+8 dBu
+6 dBu
+4 dBu
-7.79 dBu (~10 dBV)
(NOMINAL, TYP
ALL ANALOG INPUTS)

A INPUT R +	J1	6
A INPUT R -	J1	5
A INPUT R SHD	J1	4

B ANALOG R IN+
FROM pg. 2, B1
B ANALOG R IN-
MRO D4448
MRO D4448

A & B CHANNEL DIPSWITCH SETTINGS

NOMINAL INPUT LEVEL	DSW 15	DSW 16
+8 dBu	OFF	ON
+6 dBu	ON	OFF
+4 dBu	OFF	OFF
-7.79 dBu (~10 dBV)	ON	ON



APPROVALS	DATE	HARRIS CORPORATION BROADCAST SYSTEMS DIVISION		
L. STANTON	27JUNE01	DESIGNS BY PACIFIC RESEARCH & ENGINEERING		
		SCHEMATIC		
		TELCO/CODEC INPUT MODULE, BMX DIGITAL		
		SIZE	DRAWING NUMBER	REV
		D	92-1152-2	C
USED ON	BMX III DIGITAL	12-20-2001_9:43		SHEET 1 OF 3

