

1

1

A

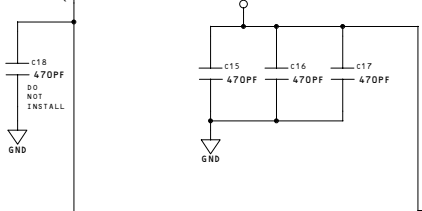
B

C

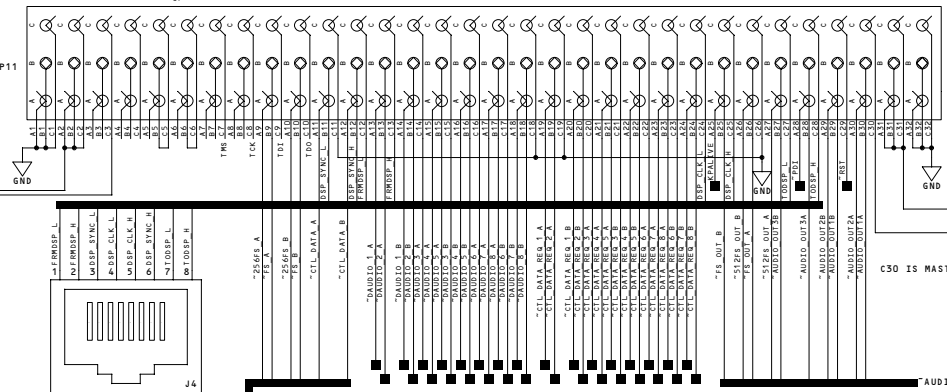
D

REVISIONS		ECO #	DESCRIPTION	DATE	APPROVAL
REV					
A			INITIAL RELEASE	6/6/03	J. DOMBROWSKI

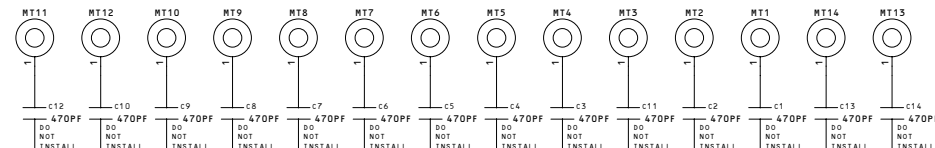
LAST DSP BOARD
BIT SELECTOR



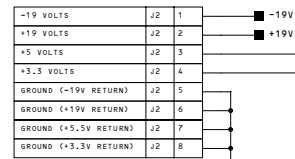
DSP CONNECTOR



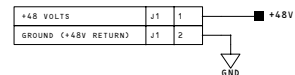
C30 IS MASTER CLOCK PIN (VCXO ENABLE); +5V IS MASTER, GND IS SLAVE.



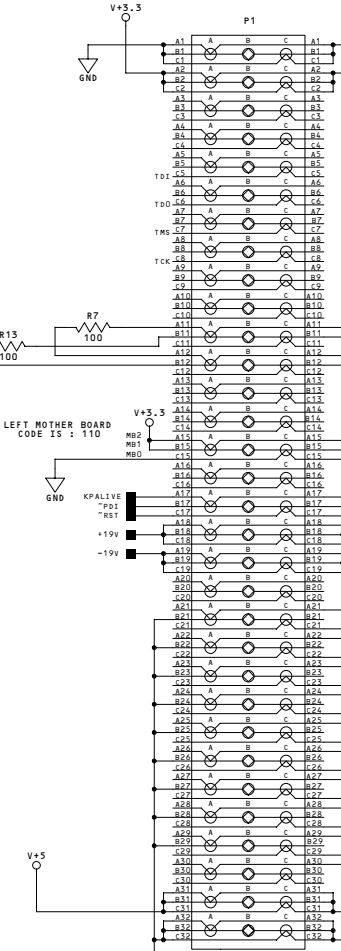
POWER CONNECTOR



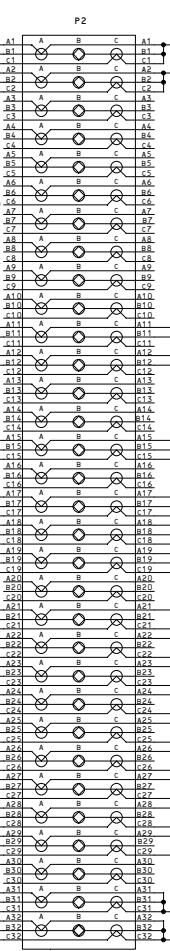
POWER CONNECTOR



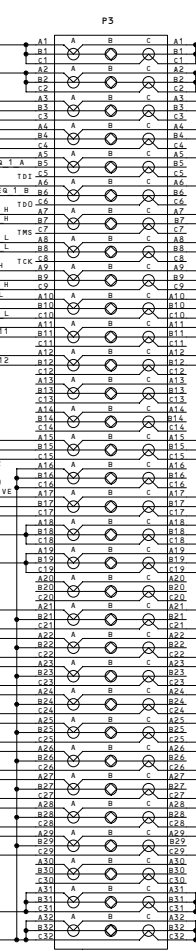
MIC MODULE 1 CONNECTOR



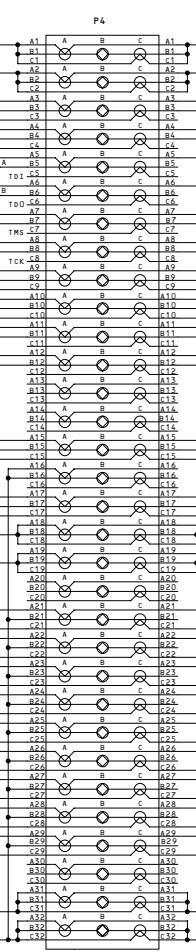
MIC MODULE 2 CONNECTOR



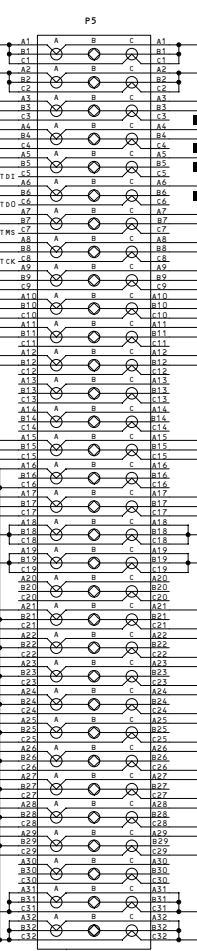
INPUT MODULE 1 CONNECTOR



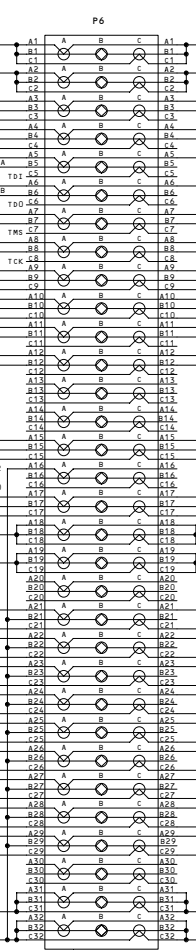
INPUT MODULE 2 CONNECTOR



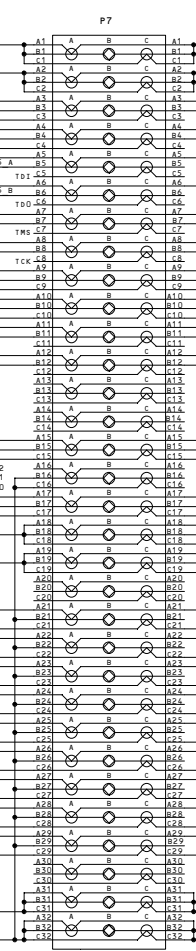
INPUT MODULE 3 CONNECTOR



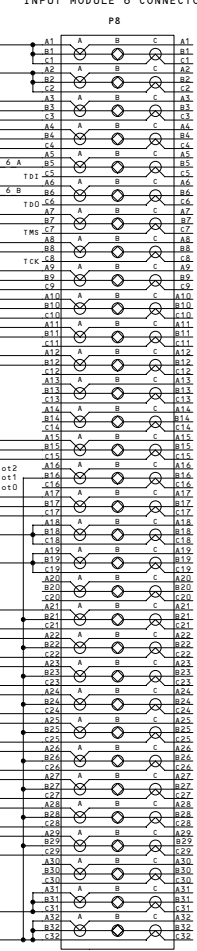
INPUT MODULE 4 CONNECTOR



INPUT MODULE 5 CONNECTOR



INPUT MODULE 6 CONNECTOR



LEFT MOTHER BOARD
CODE IS : 110



STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

STRAIGHT, FEMALE, DIN, THOLE, SOLDER

GND

GND

GND

GND

GND

GND

GND

GND

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

R10 680

R10 680

R10 680

R10 680

R10 680

R10 680

R10 680

R10 680

R11 115

R11 115

R11 115

R11 115

R11 115

R11 115

R11 115

R11 115

R2 100

R2 100

R2 100

R2 100

R2 100

R2 100

R2 100

R2 100

R4 100

R4 100

R4 100

R4 100

R4 100

R4 100

R4 100

R4 100

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

V+3.3

R12 1.2K

R12 1.2K

R12 1.2K

R12 1.2K

R12 1.2K

R12 1.2K

R12 1.2K

R12 1.2K

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R1 100

R1 100

R1 100

R1 100

R1 100

R1 100

R1 100

R1 100

R3 100

R3 100

R3 100

R3 100

R3 100

R3 100

R3 100

R3 100

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R5 100

R5 100

R5 100

R5 100

R5 100

R5 100

R5 100

R5 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R6 100

R6 100

R6 100

R6 100

R6 100

R6 100

R6 100

R6 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R7 100

R7 100

R7 100

R7 100

R7 100

R7 100

R7 100

R7 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R8 100

R8 100

R8 100

R8 100

R8 100

R8 100

R8 100

R8 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R9 100

R9 100

R9 100

R9 100

R9 100

R9 100

R9 100

R9 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R10 100

R10 100

R10 100

R10 100

R10 100

R10 100

R10 100

R10 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

V+5

V+5

V+5

V+5

V+5

V+5

R11 100

R11 100

R11 100

R11 100

R11 100

R11 100

R11 100

R11 100

GND

GND

GND

GND

GND

GND

GND

GND

V+5

V+5

1

2

3

4

1

2

3

4

"AUDIO_OUT1A","AUDIO_OUT1B","AUDIO_OUT2A","AUDIO_OUT2B","AUDIO_OUT3A","AUDIO_OUT3B","512FS_OUT_A","512FS_OUT_B","FS_OUT_A","FS_OUT_B

