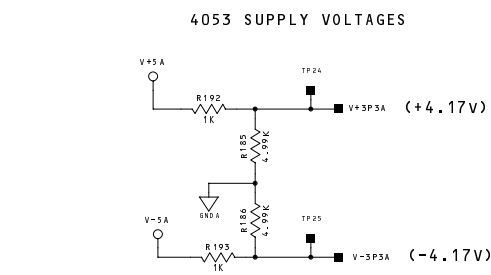
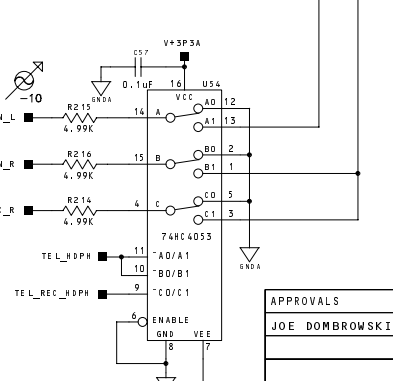
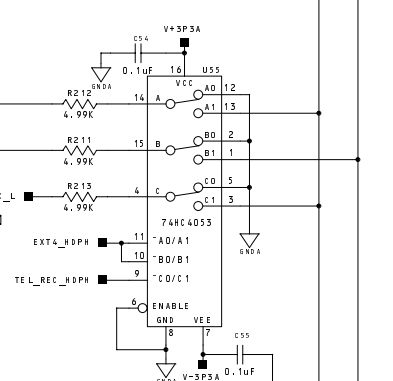
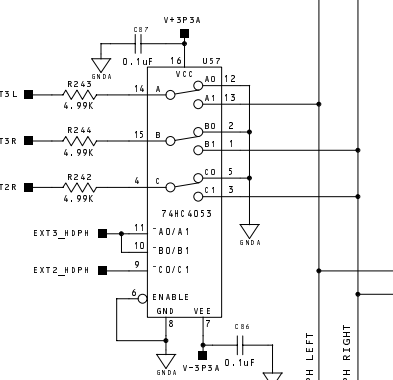
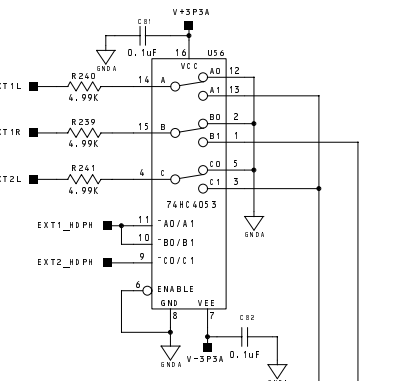
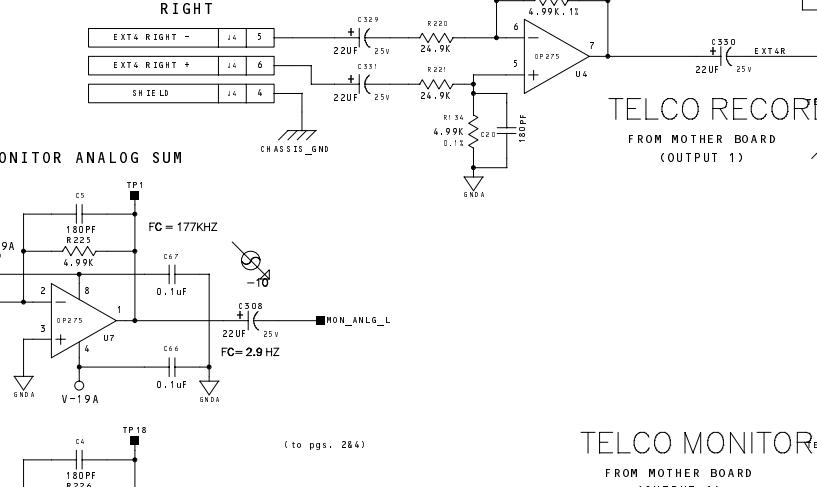
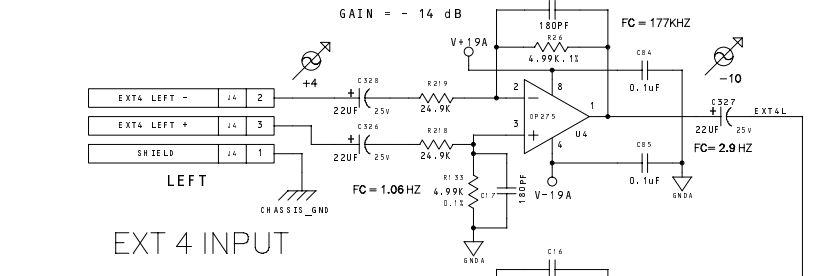
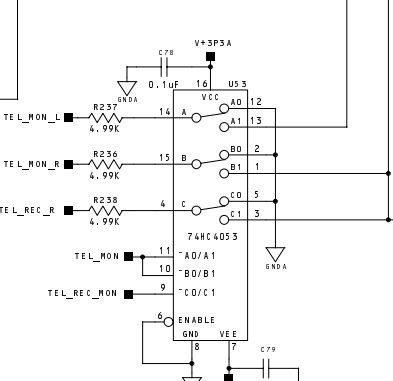
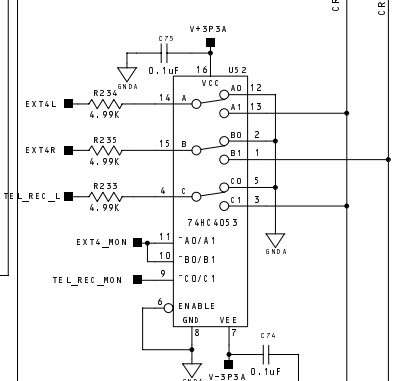
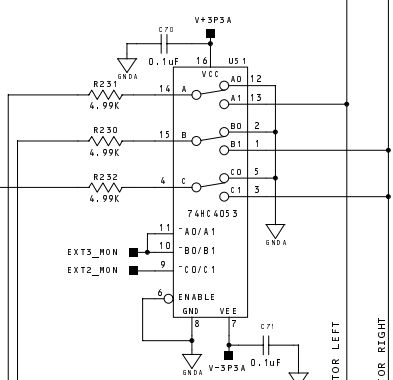
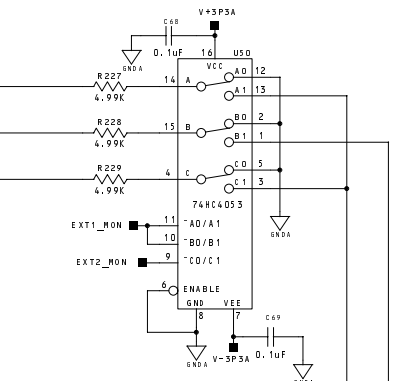
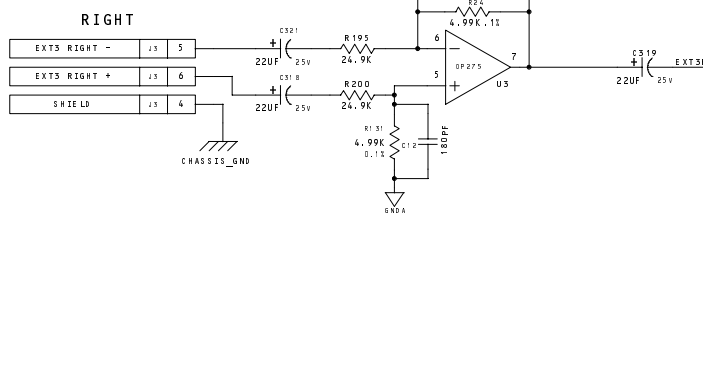
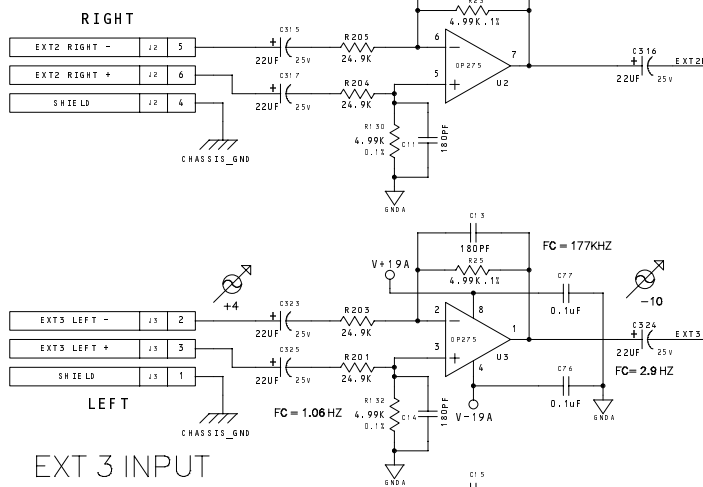
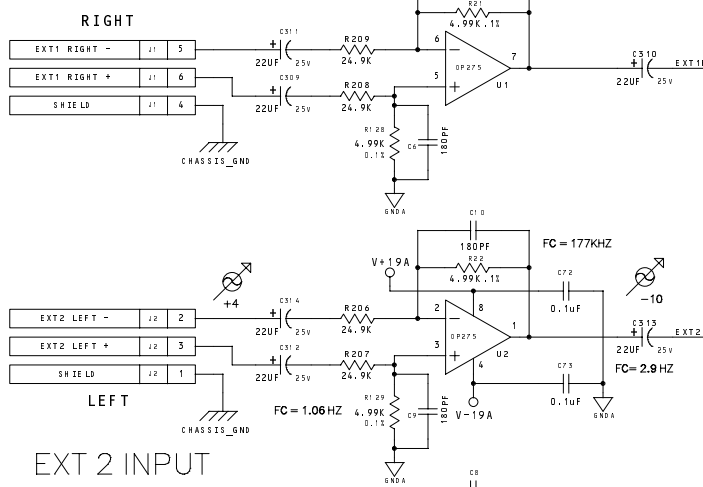
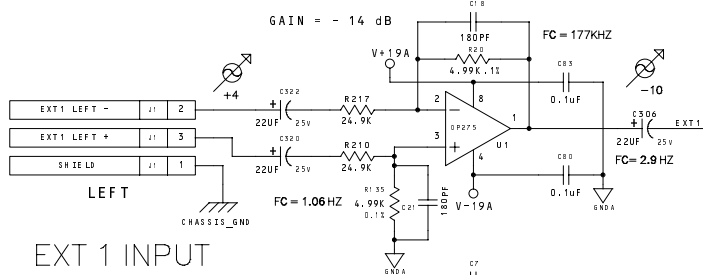


EXTERNAL and TELCO INPUTS

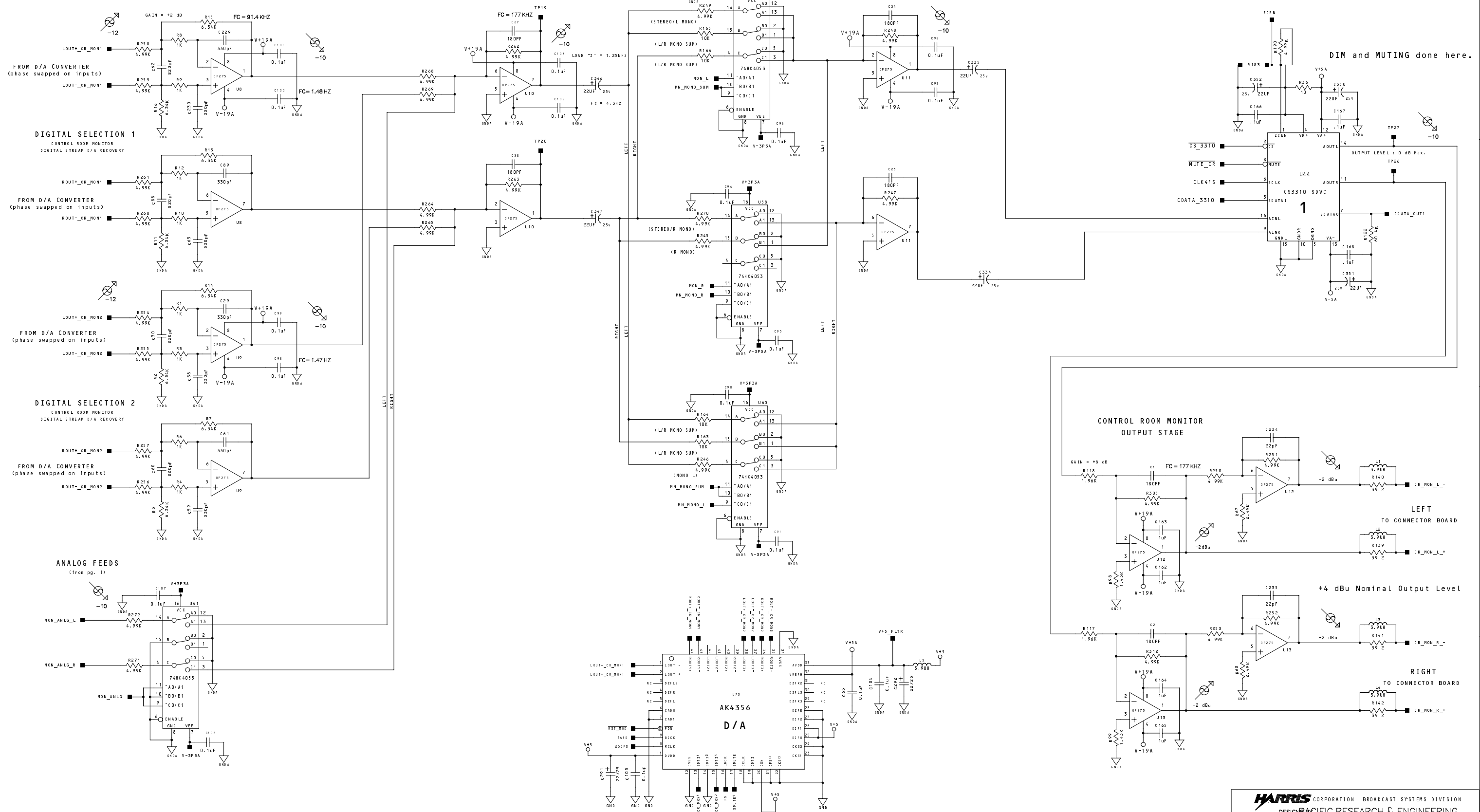
REVISIONS				
REV	ECO #	DESCRIPTION	DATE	APPROVAL
A		INITIAL RELEASE	8/29/01	J. Dombrowski
B	4656	DESIGN ERRORS	8/21/01	J. Dombrowski
C	4704	SDVC zero-crossing issue	10/10/01	J. Dombrowski
D	4727	ADD RESISTOR NETWORK @ 183	2/12/01	D. NGUYEN



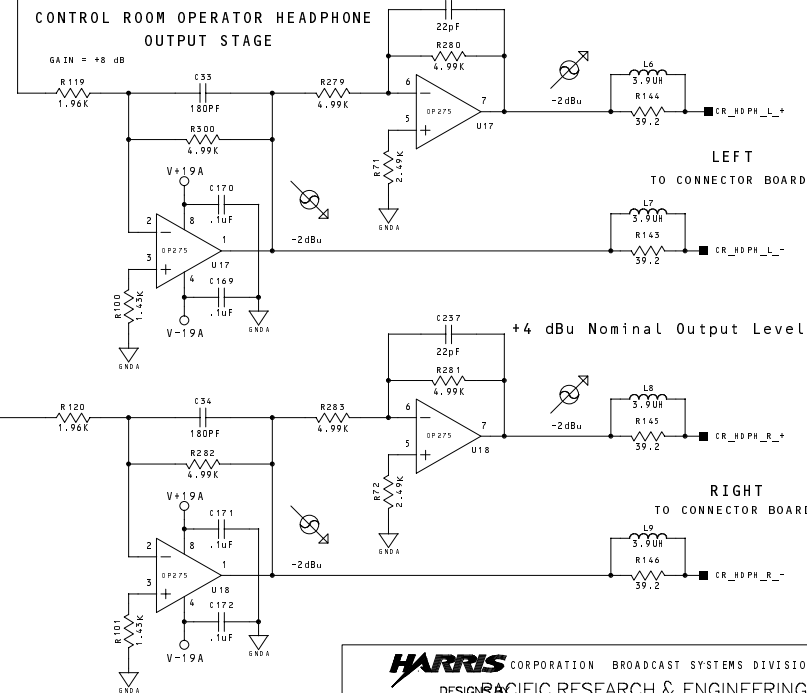
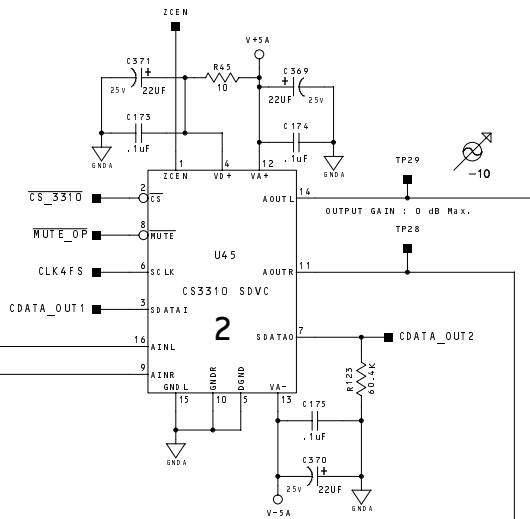
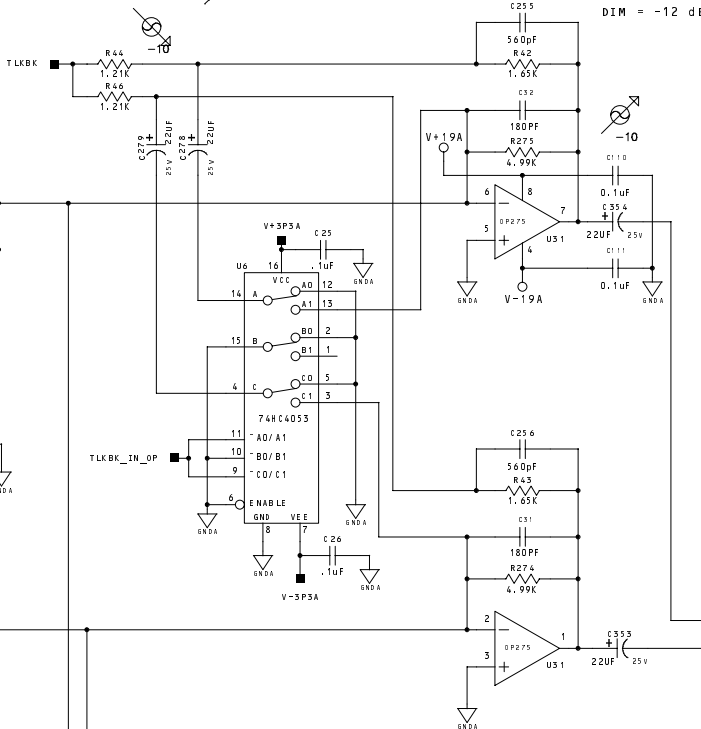
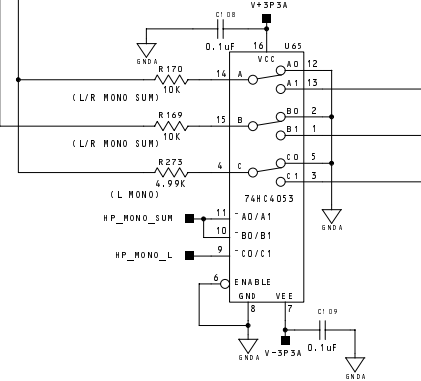
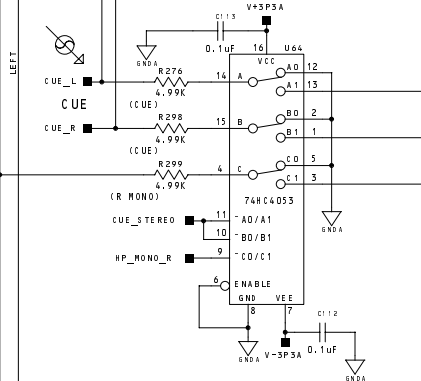
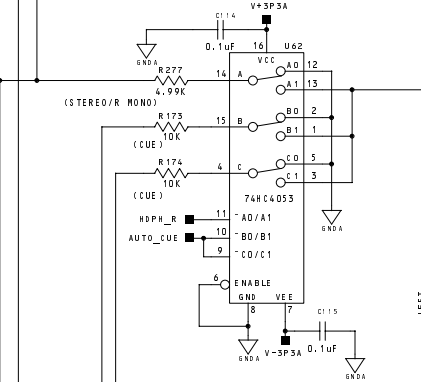
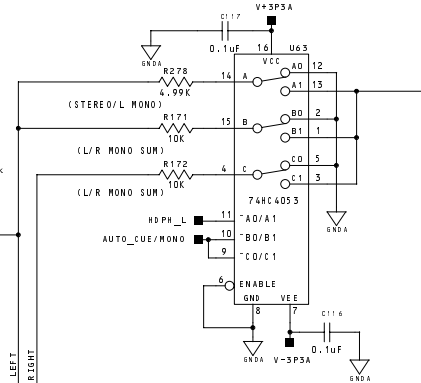
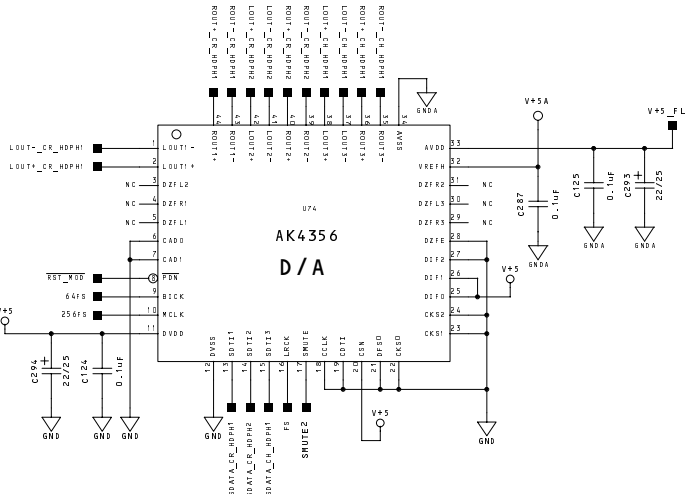
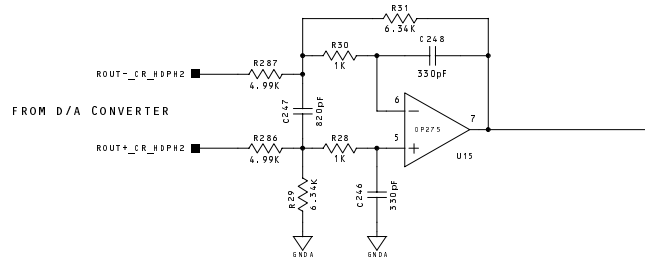
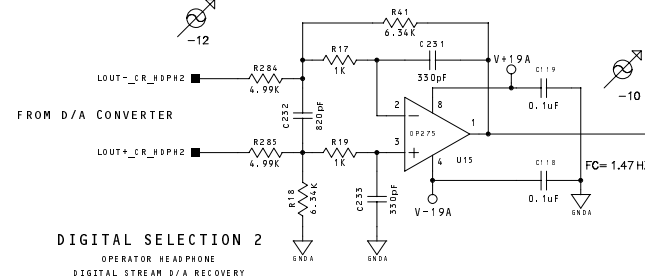
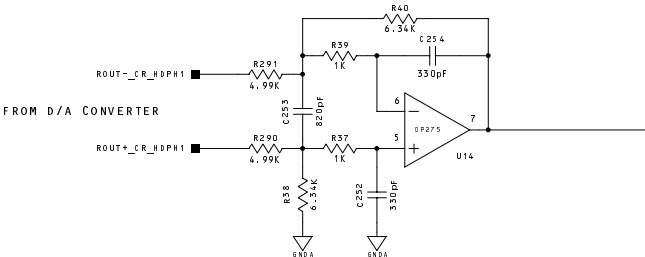
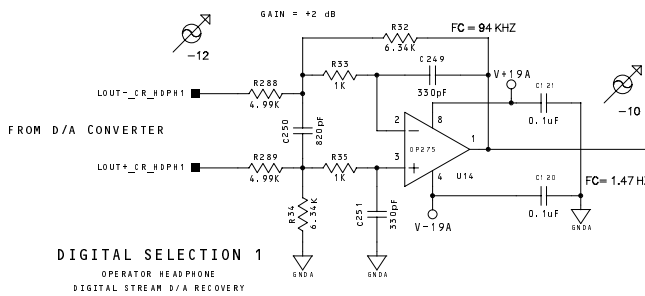
APPROVALS		DATE		<div><div>HARRIS</div><div>DESIGNS BY</div></div> <div>CORPORATION BROADCAST SYSTEMS DIVISION PACIFIC RESEARCH & ENGINEERING</div>	
JOE DOMBROWSKI		8/28/01			
				SCHEMATIC	
				CONTROL ROOM MONITOR BOARD	
		SIZE D		DRAWING NUMBER 92-1157	
				REV D	
USED ON		BMX DIGITAL		12-20-2001_9:36	
				SHEET 1 OF 7	

CONTROL ROOM MONITOR

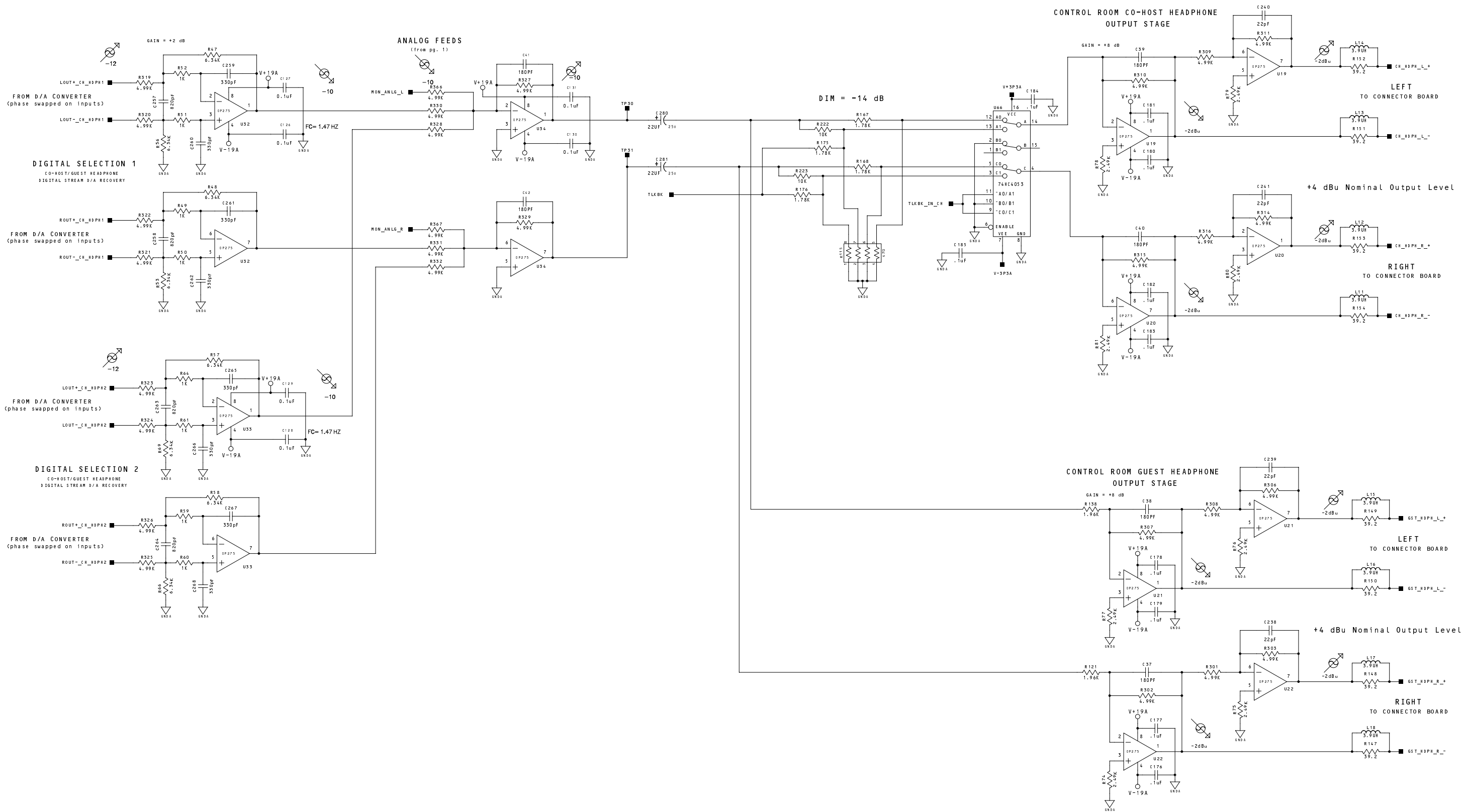
D/A OUTPUT LEVEL : 5.5 Vpp Diff Full Scale = 2.75 Vp = 1.95 Vrms = +7.97 dBu = " +8 dB
CONSOLE INTERNAL FULL SCALE LEVEL : +18 dBu ; +16 dB GAIN is required for DA output level.
CONSOLE INTERNAL NOMINAL LEVEL : -10 dBu ; +2 dB GAIN is required for DA output level.



CONTROL ROOM (OPERATOR) HEADPHONE

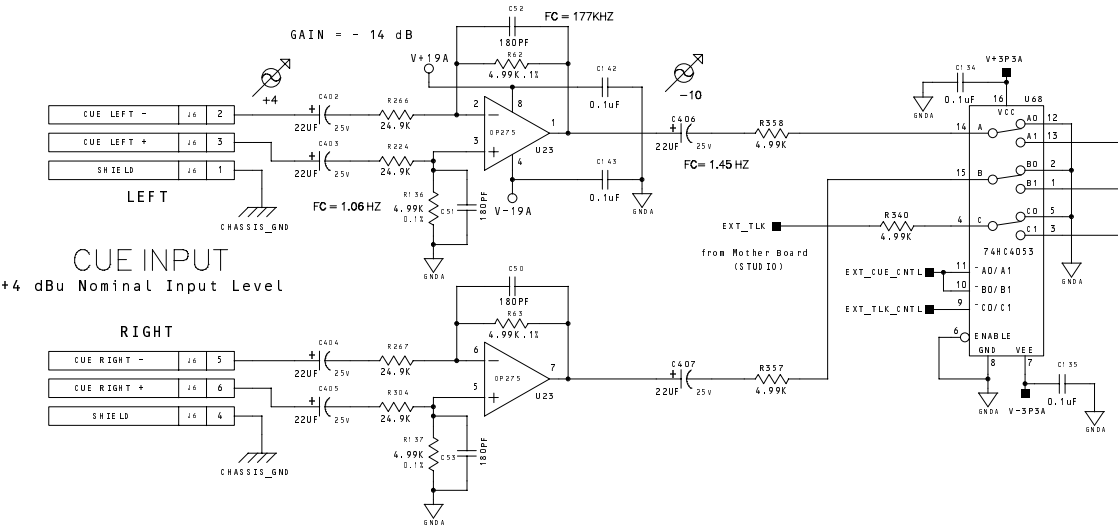


CO-HOST and GUEST HEADPHONE

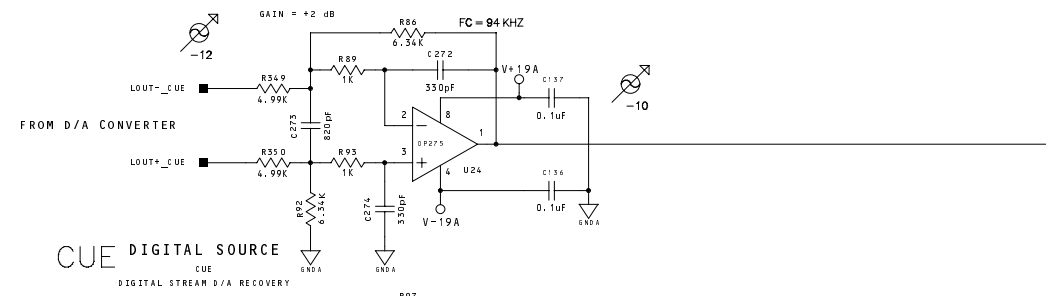


CUE and TALKBACK

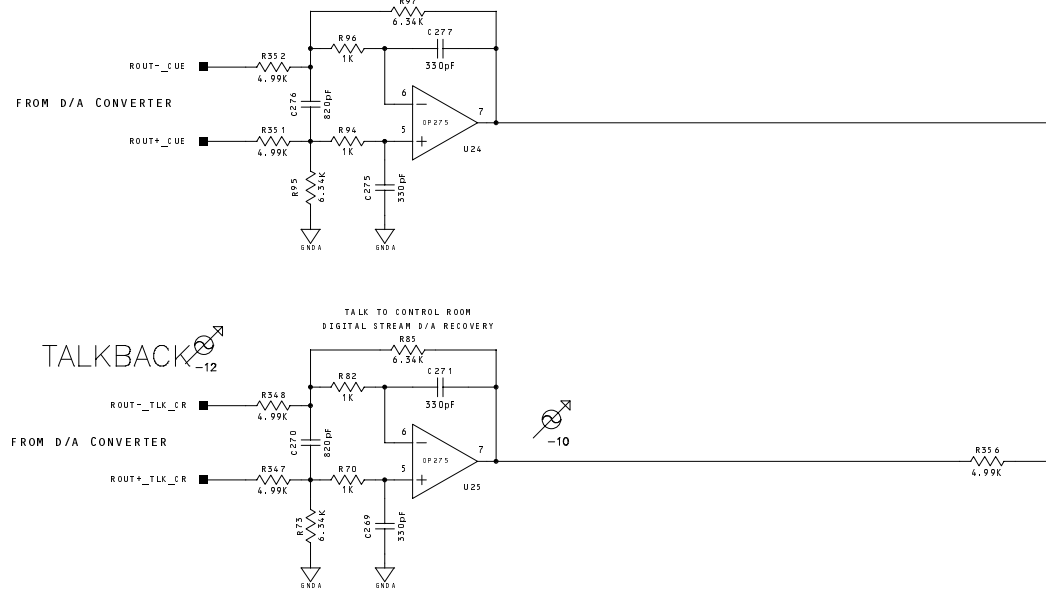
1



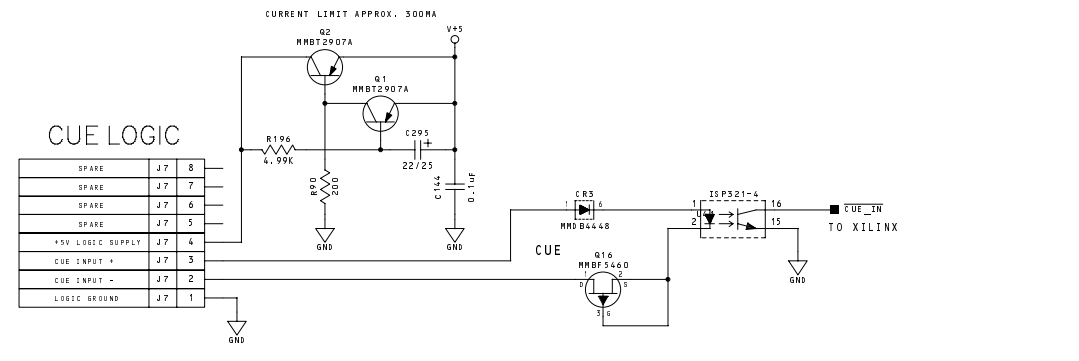
2



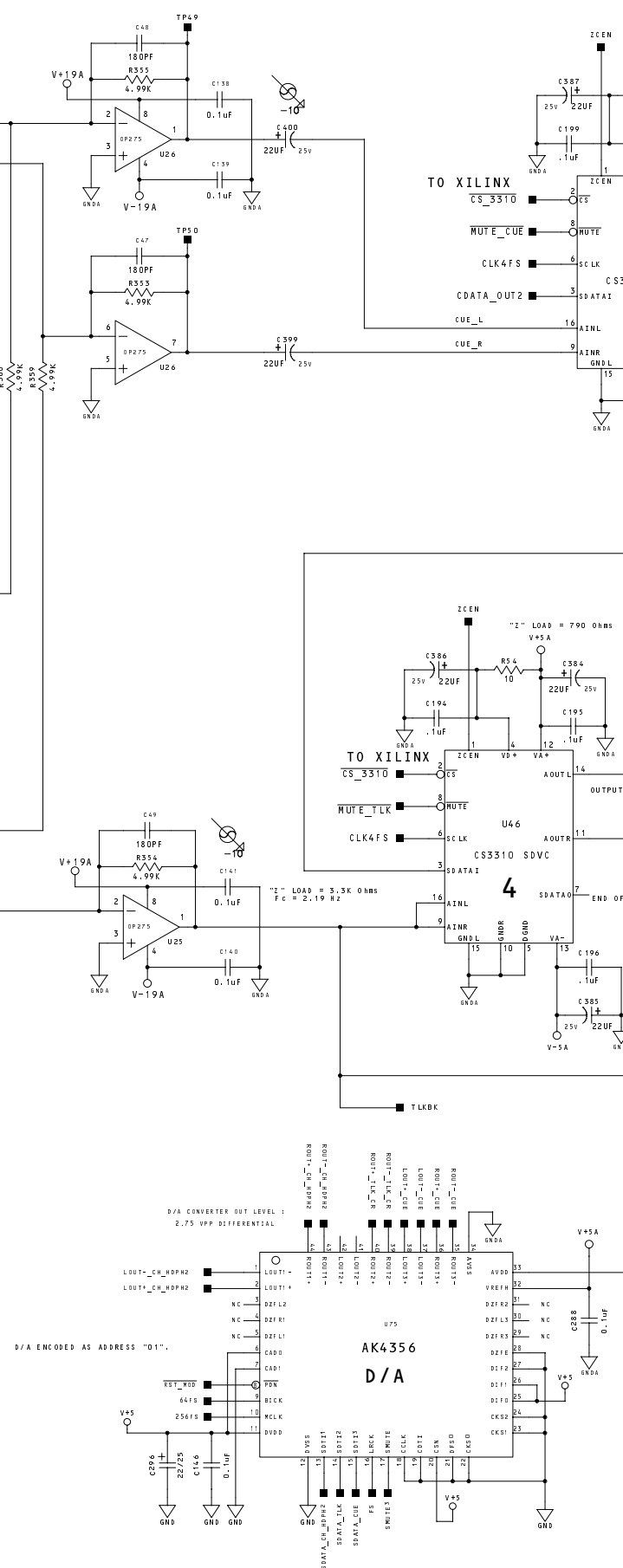
3



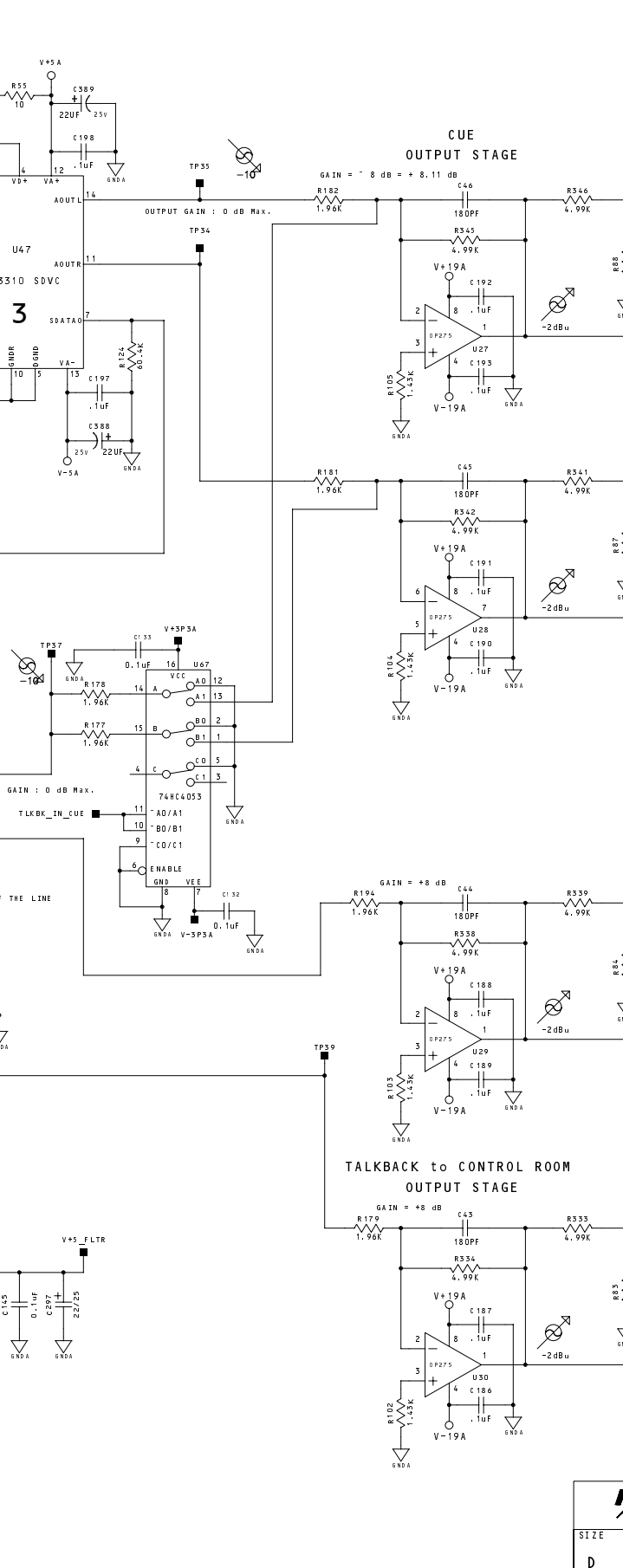
4



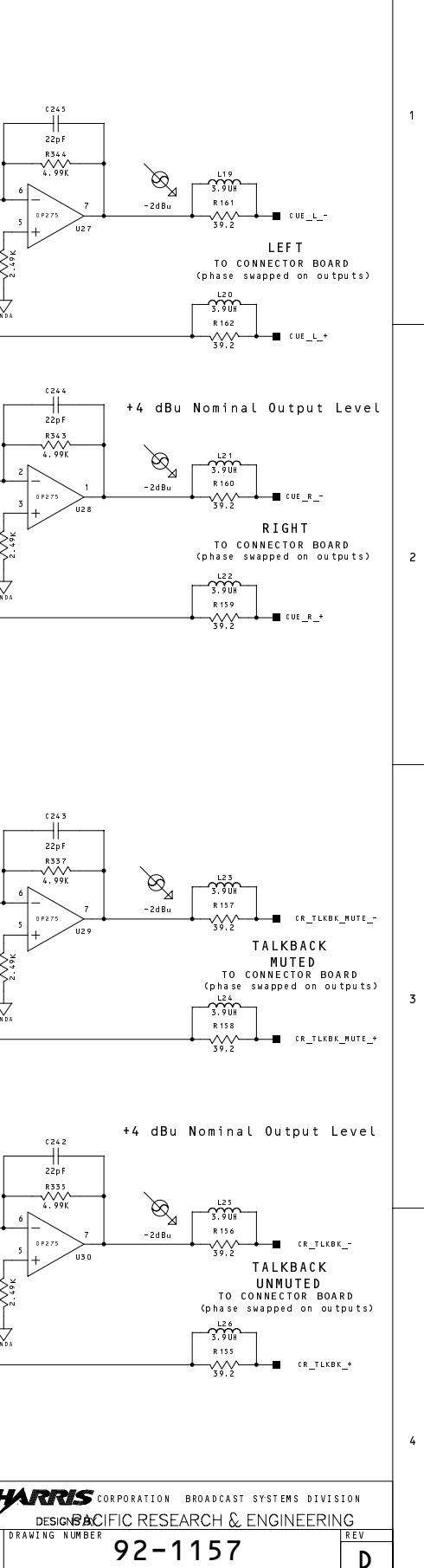
B



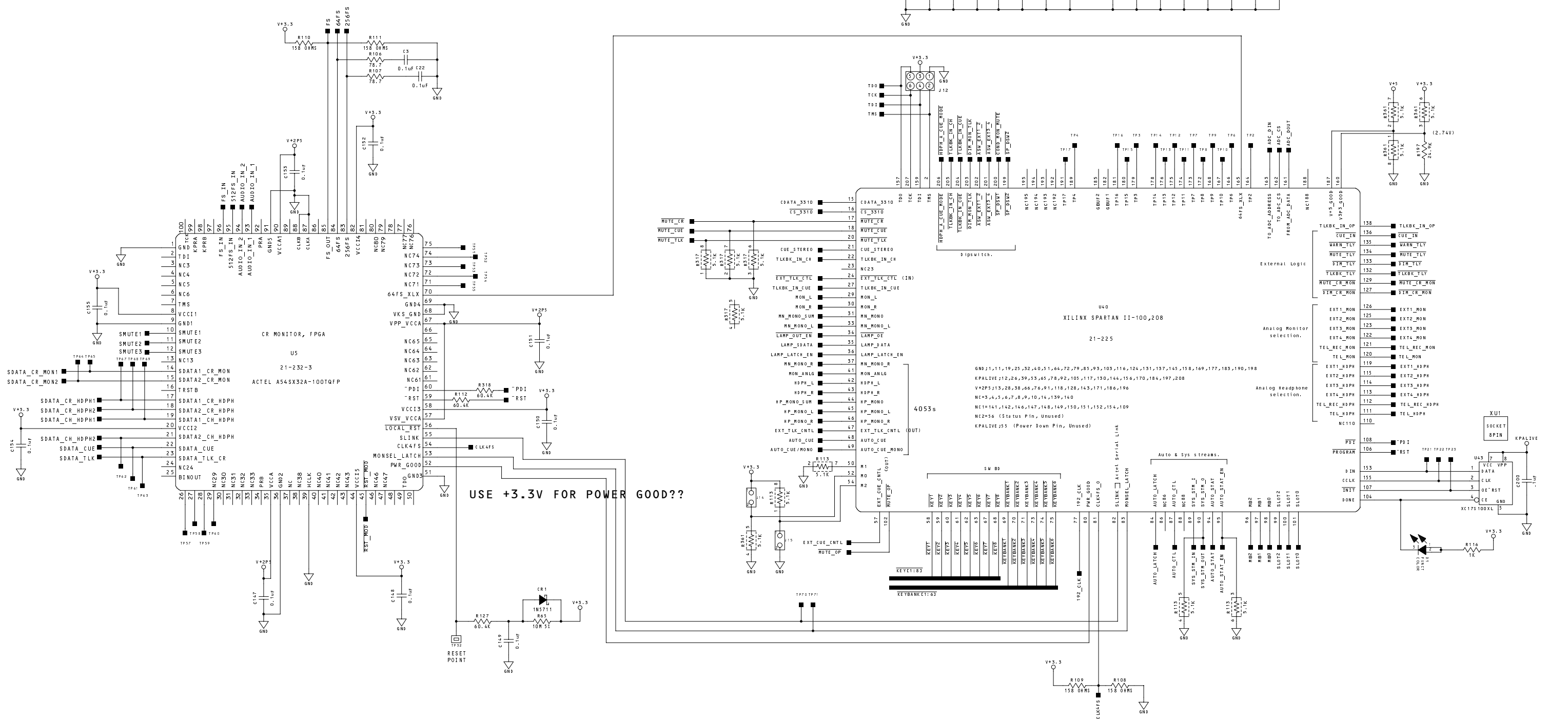
C



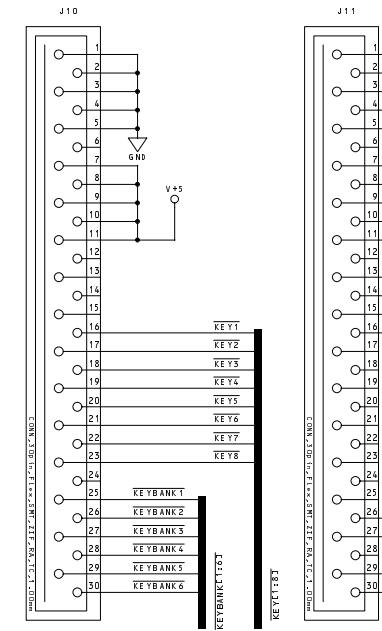
D



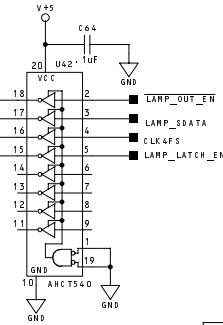
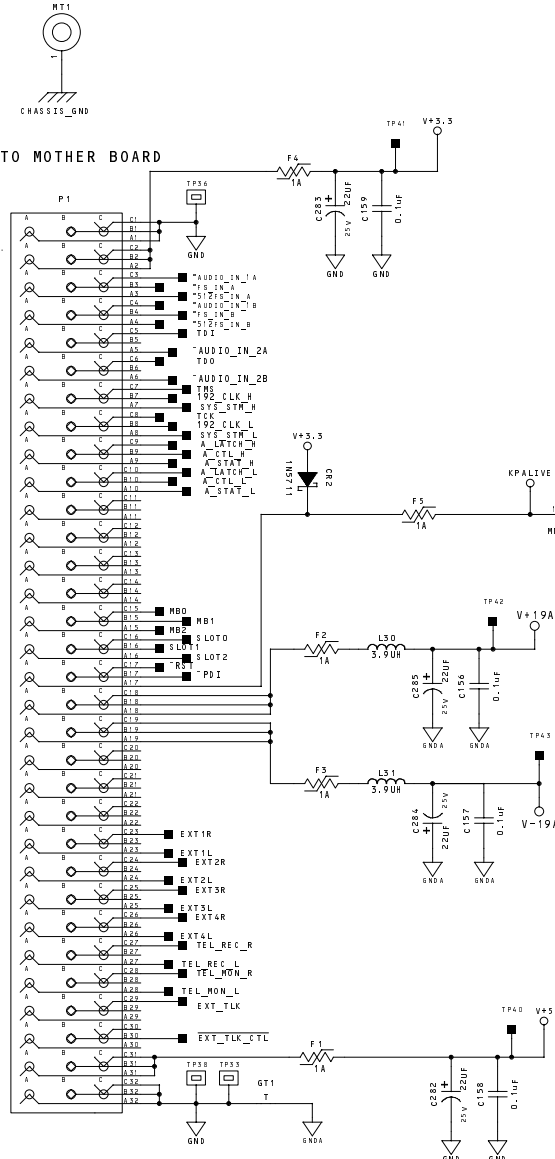
FPGAs



INPUTS and OUTPUTS

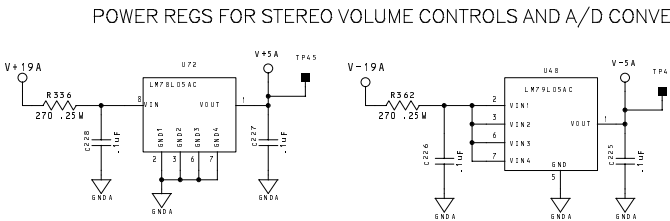
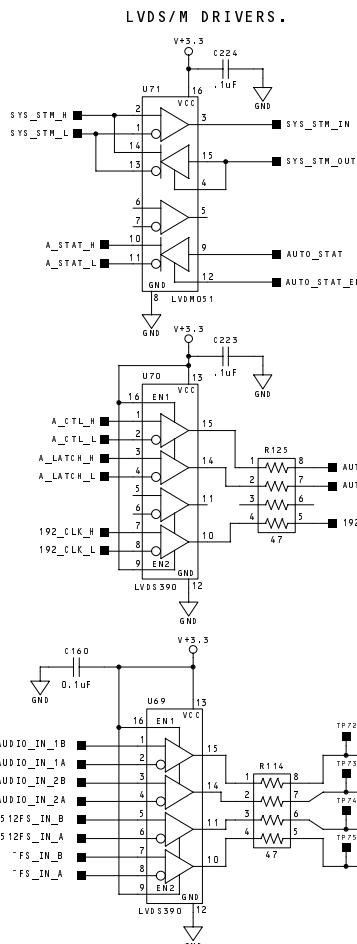


TO XILINX

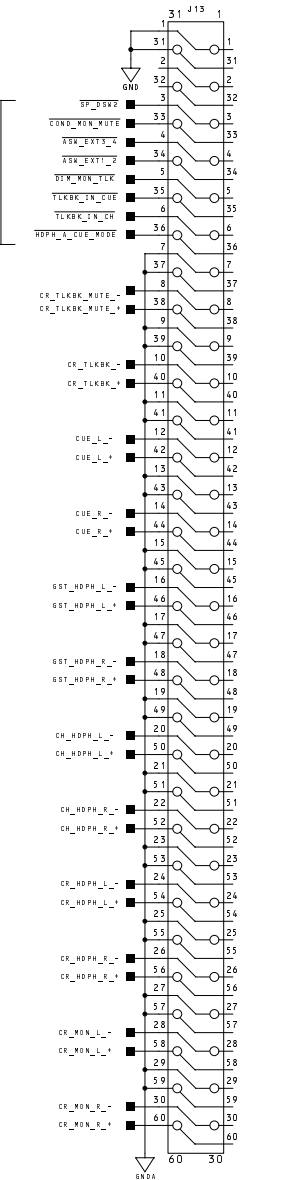


HEADPHONES

MONITOR



TO CONNECTOR BOARD



CTL OPTO-ISO V INPUT +	25	1
TALKBACK RELAY	25	1
DIM RELAY	25	1
MUTE RELAY	25	1
MU, DIM & TB RLY COM	25	1
DIM INPUT -	25	1
MUTE INPUT -	25	1
+5V LOGIC SUPPLY	25	1
+5V LOGIC SUPPLY	25	1
WARNING RELAY	25	1
WARNING RELAY	25	1
LOGIC GROUND	25	1
LOGIC GROUND	25	1
LOGIC GROUND	25	1

